

Abstract of the Disclosure

Disclosed is a driving voltage controller of sense amplifiers for a memory device which prevents the sense amplifier from being over-driven when an external power voltage is used for an internal driving voltage of the sense amplifier. Reference voltage generator generates a reference voltage. Core voltage generator generates a core voltage to be used for driving voltage of sense amplifier. Comparator compares the core voltage generated by core voltage generator with the reference voltage generated by reference voltage generator. Clamp adjusts a level of the core voltage generated by the core voltage generator based on an output signal of comparator. In the case where an external power is used for a driving voltage of the sense amplifier is excessively high, the comparator compares the external power voltage with the reference voltage. When the driving voltage of sense amplifier exceeds the reference voltage, the clamp drops the driving voltage of sense amplifier. Thus, the core voltage can be stably supplied.